

GSSSINSTITUTE OF ENGINEERING & TECHNOLOGY FOR WOMEN





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AICTE Sponsored

5th International Conference on Electrical, Electronics, Communication, Computer Technologies and Optimization Techniques





Time: 11:30 AM - 2:00PM

Google Meet Link: https://meet.google.com/rjy-qyyn-dmx

Day- 2		SESSION: 11	Session Coordinator : Chethan H R, Asst. Prof, Dept. of EEE	
SI. No	Paper ID	Author Name	Paper Title	Time
1	135	G V Ganesh, Manindra Raavi	Implementation and comparison of Boolean logic for different Nano Scaling technologies	11:30 AM – 11:45 AM
2	296	Raksha S,Graceline Jasmine S,Jani Anbarasi L,Prasanna M, Kamaleshkumar S	Weather Forecasting Framework for Time Series Data using Intelligent Learning Models	11:45 AM – 12 AM
3	245	Haseena Kuttomparambil Abdulkhader	Design of robust fractional order lead-lag controller using a hybrid dynamic GA-BFA algorithm for power system oscillation damping	12 AM – 12:15 PM
4	93	Nour eldin safwat, Ismail M. Hafez Fatma Newagy	Air-to-Air channel model for UAVs in Dense Urban Environments	12:15 PM- 12:30 PM
5	166	Aruna Rao B P, Dr.Kiran A Gupta	Advances in 3D NoC Based Multi-Core Systems Research and IO Design: Past, Present and Future - A Systematic Review	12:30 PM- 12:45 PM
6	148	Vadim Petrov	Mathematical tools of parametric identification algorithms based on the Fourier spectral model in the Chebyshev-Hermite functions basis	12:45 PM- 1:00PM
7	295	Venkata Krishna Rao Maddela, Bhaskararao Peri	Realization of Telugu r-phoneme in intervocalic position: An acoustic study	1:00 PM- 1:15 PM
8	226	Tharun Kumar Guduru Avula Manoj Kumar Reddy Papanasam Esakki, Manikandan Nagarajan	Energy Efficient Low Complexity Inexact Adders for Error Tolerant Applications	1:15 PM- 1:30 PM
9	297	Ratnesh Kumar	Bond Dissociation Energy and Pattern Spectrum for Shape Classification	1:30 PM- 1:45 PM
10	41	Rolf Drechsler	Polynomial Circuit Verification using BDDs	1:45 PM- 2:00 PM